



· In re application of

Confirmation No. 4173

Kazuhiro NISHIKAWA et al.

Docket No. 2003\_0915A

Serial No. 10/611,868

Group Art Unit 3729

Filed July 3, 2003

Examiner C.J. ARBES

MULTILAYERED CIRCUIT BOARD FORMING METHOD AND MULTILAYERED CIRCUIT BOARD

## **RESPONSE**

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 THE COMMISSIONER IS AUTHORIZED TO CHARGE ANY DEFICIENCY IN THE FEES FOR THIS PAPER TO DEPOSIT ACCOUNT NO. 23-0975

Sir:

Responsive to the Office Action mailed January 26, 2005, please amend the above-identified application as follows: